

COFC



**TRANSMITTAL LETTER**  
**(General - Patent Pending)**

Docket No.  
**FIS920030098US1**

Re Application Of: **Freeman et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/604,212	7/1/2003	George C. Eckert, II	45094	2812	1211

Title: **BIPOLAR TRANSISTOR SELF-ALIGNMENT WITH RAISED EXTRINSIC BASE EXTENSION AND METHODS OF FORMING THE SAME**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

**Certificate of Correction with Patent Office Error Noted**

**Certificate**  
**NOV 30 2005**  
**of Correction**

in the above identified application.

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Dated: **November 23, 2005**

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on <b>November 23, 2005</b> (Date)	
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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,960,820

DATED : November 1, 2005

INVENTOR(S) : Freeman et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 8, Column 9, Line 40 please take out the word "separate" and insert - - separation - -

Claim 17, Column 10, Line 48 please insert - - base - - in between the words "intrinsic" and "positioned"

Claim 17, Column 10, Line 51 please take out the word "position" and insert - - positioned - -

MAILING ADDRESS OF SENDER (Please do not use customer number)

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PATENT NO. 6,960,820

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